PATENT NUMBER

JC825 U.S. PTO
09/727188
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JC 844

# U.S. UTILITY Patent Application

| O.I.P.E. | PATENT DATE |
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| M.H SCANNED Q.A. TM | |

| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
|---|---|---|---|---|---|
| 09/727188 | | ~~740~~ 703 | 16 | ~~2784~~ 2125 | PALADINAN |

**APPLICANTS**

Clinton Ramsey

**TITLE**

Method and apparatus for generating transaction-based stimulus for simulation of VLSI circuits using event coverage analysis

PTO-2040
12/99

## ISSUING CLASSIFICATION

| ORIGINAL | | | CROSS REFERENCE(S) | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| CLASS | SUBCLASS | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | | | | | |
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| INTERNATIONAL CLASSIFICATION | | | | | | | | | |
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| TERMINAL ☐ DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
|---|---|---|---|---|---|
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| ☐ The term of this patent subsequent to _____ (date) has been disclaimed. | (Assistant Examiner)          (Date) | | | NOTICE OF ALLOWANCE MAILED | |
| ☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ | | | | ISSUE FEE | |
| | (Primary Examiner)          (Date) | | | Amount Due | Date Paid |
| ☐ The terminal ___ months of this patent have been disclaimed. | (Legal Instruments Examiner)          (Date) | | | ISSUE BATCH NUMBER | |

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